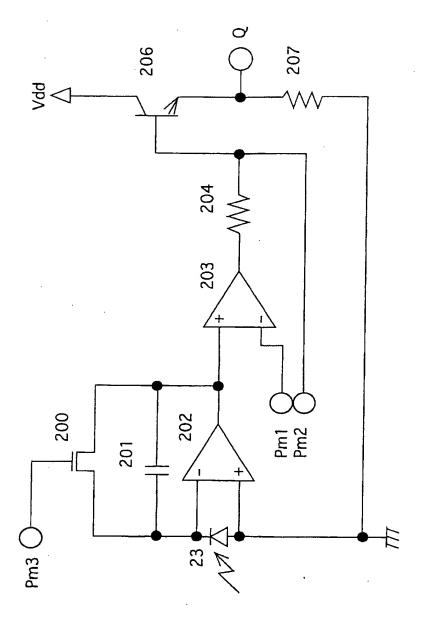


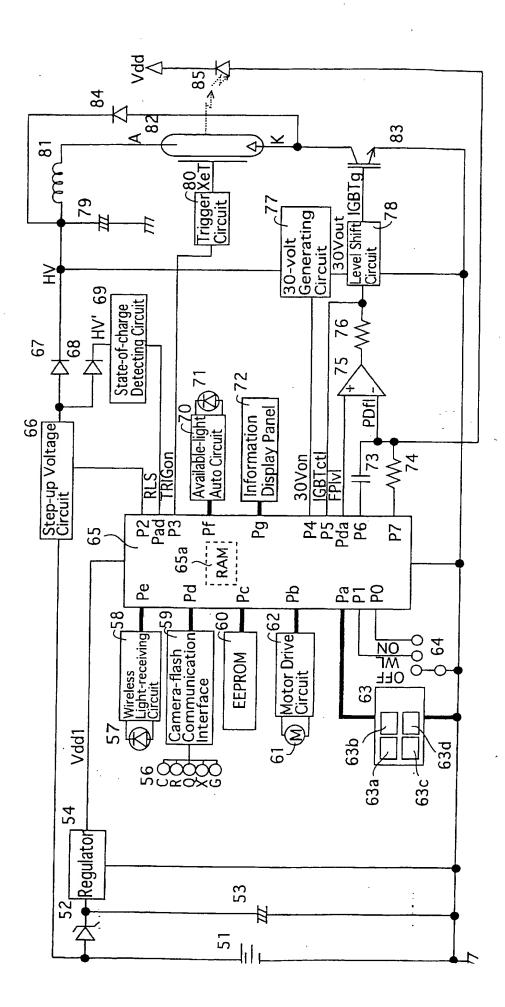
Fig. 3



20

Fig. 4A

20



The first term of the

Fig. 4B

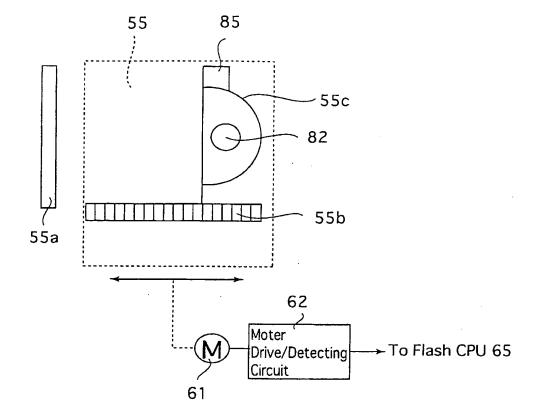


Fig. 5

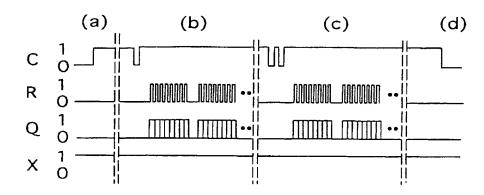
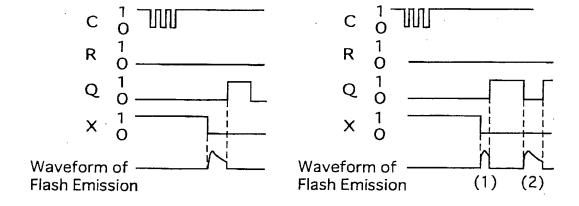
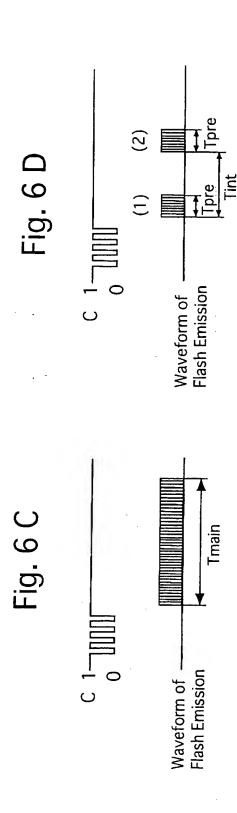


Fig. 6 A

Fig. 6 B





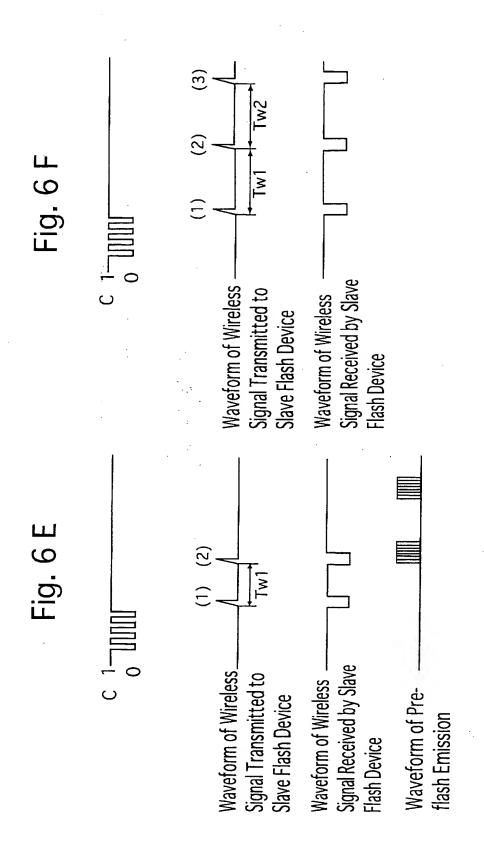


Fig. 7

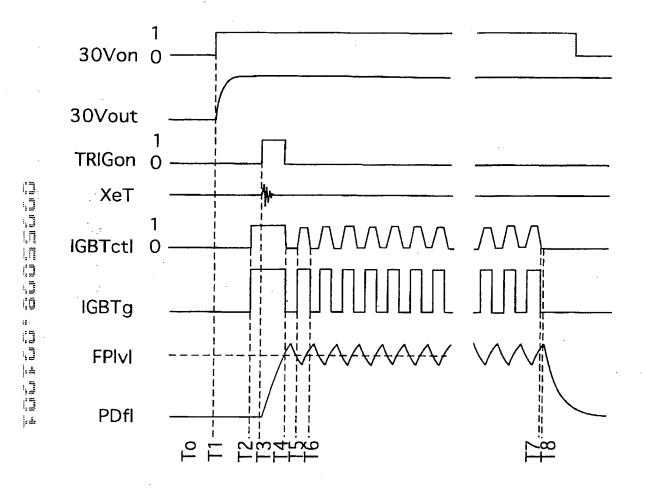


Fig.8A

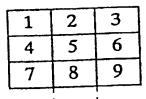


Fig.8B

Fig.8C

4	12	4
12	36	12
4	12	4

Fig.9A

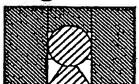


Fig.9B

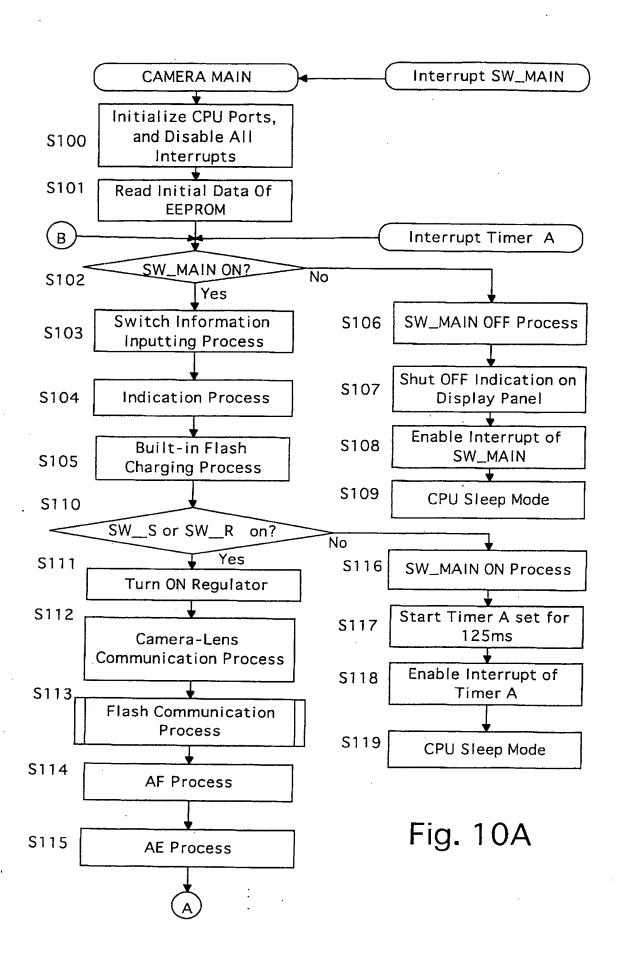
1	1	1
1	4	1
1	3	1

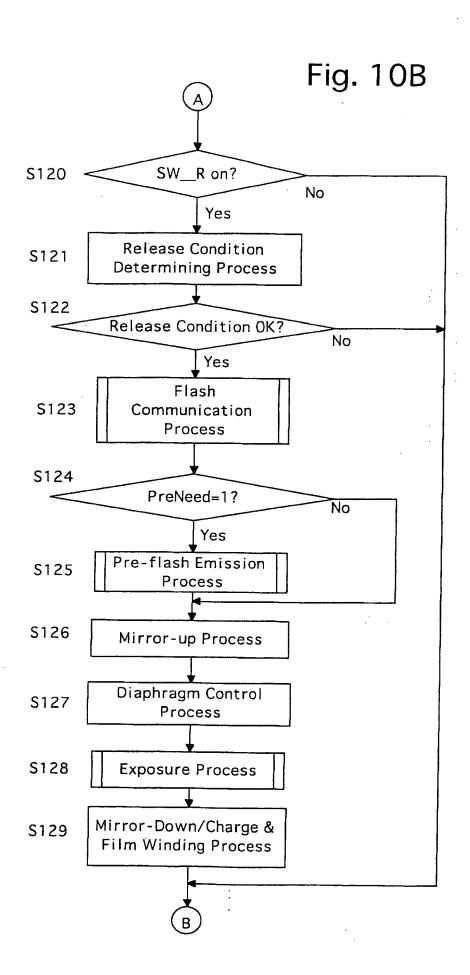
Fig.9C

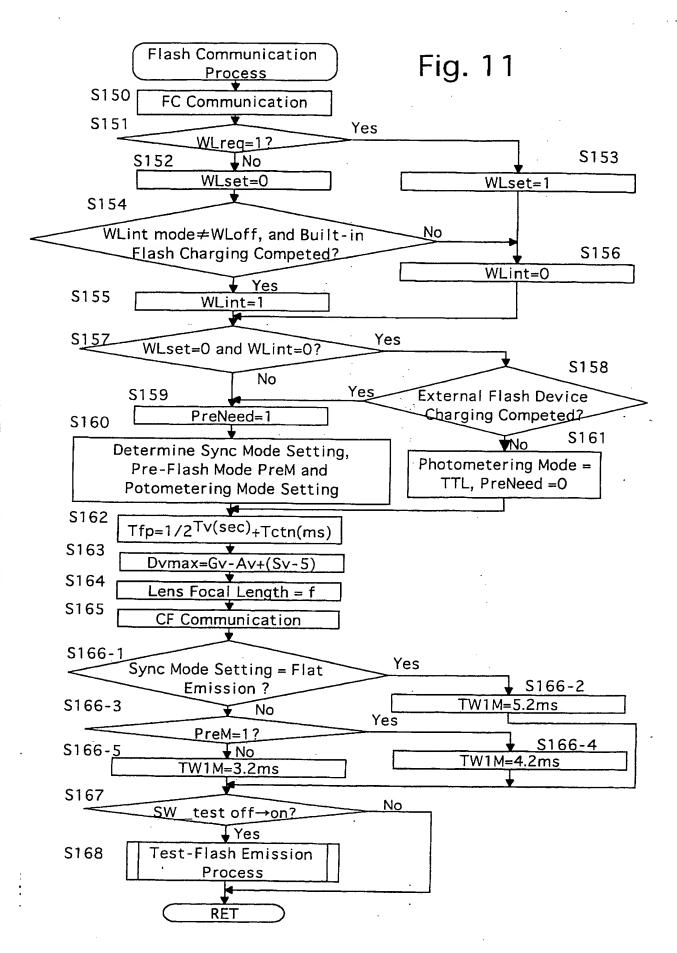


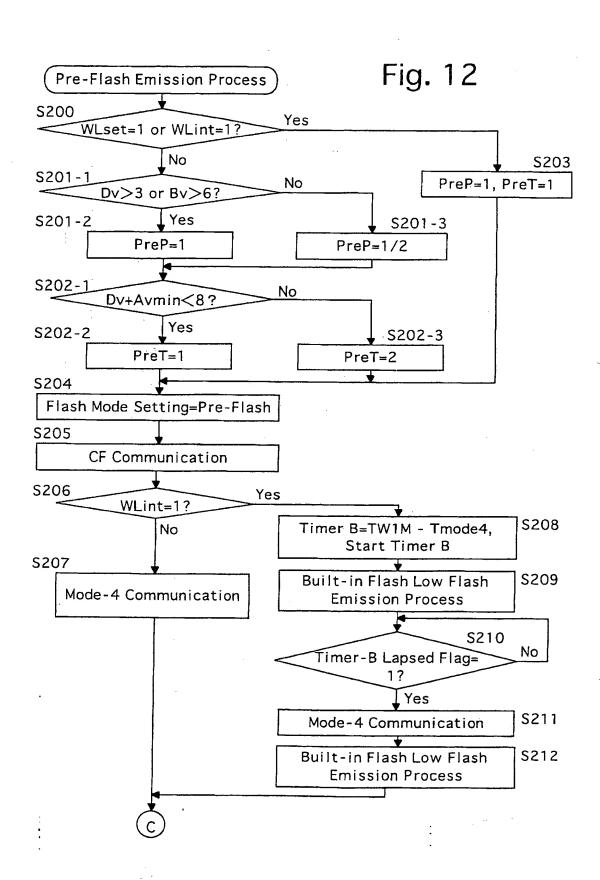
Fig.9D

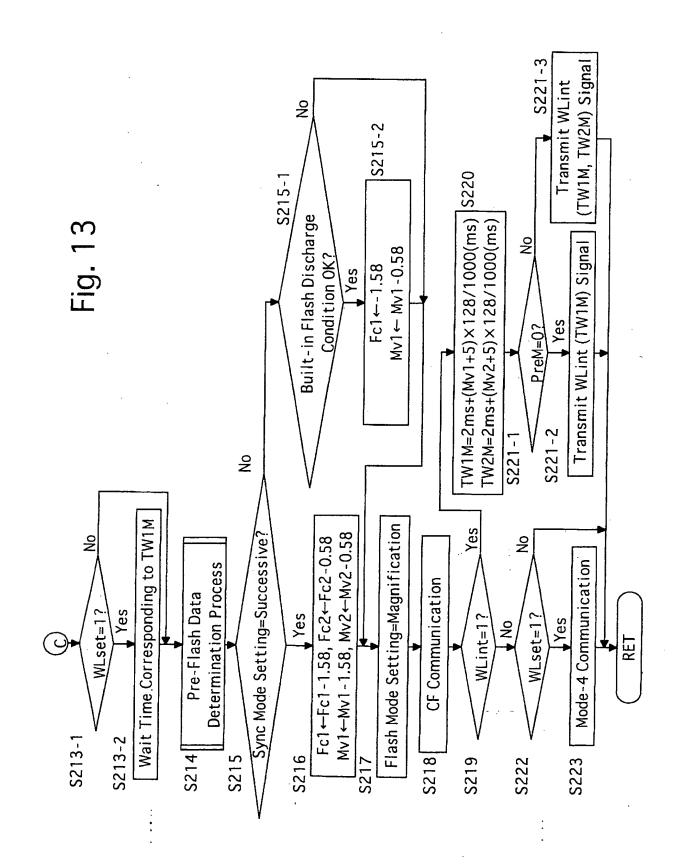
4	4	7.
4	4	7
4	4	7

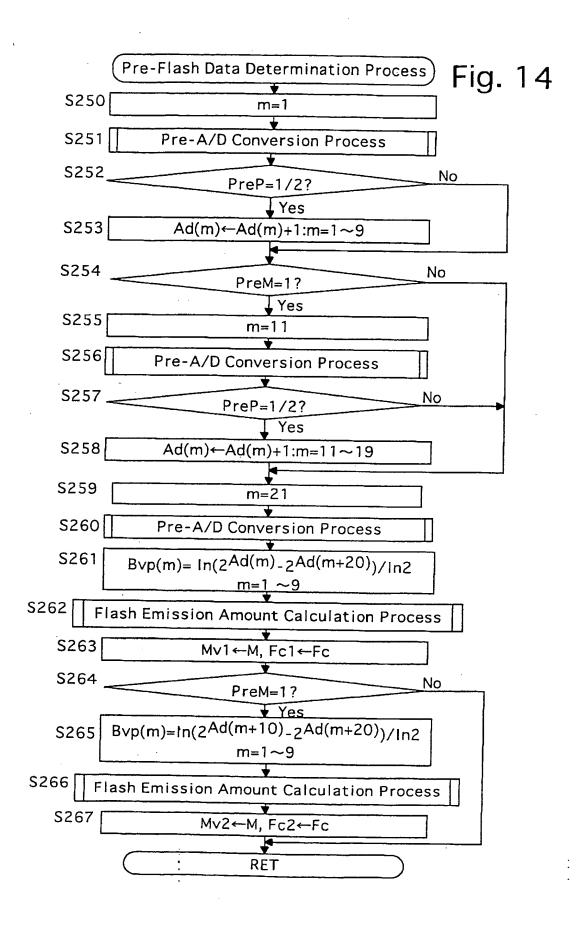


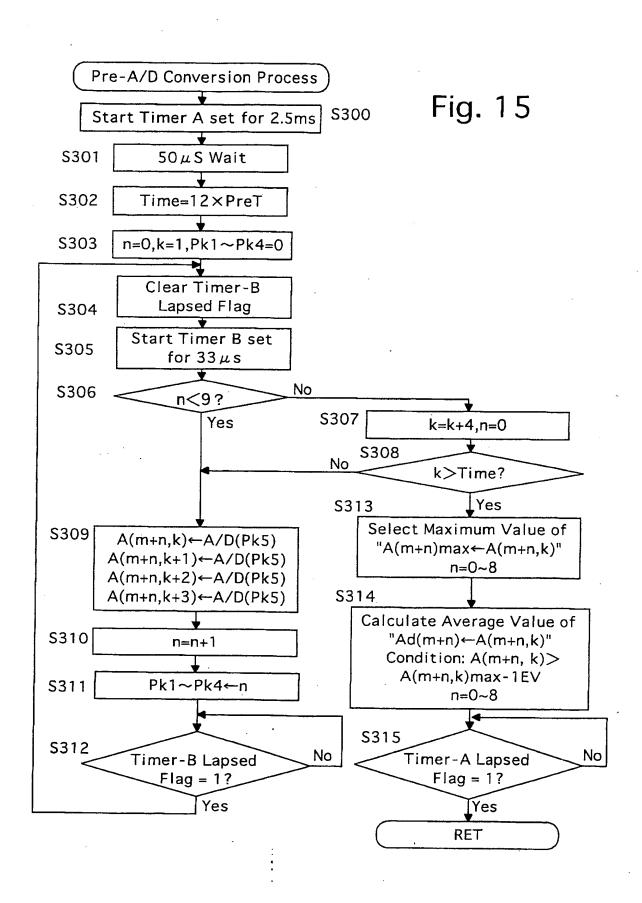


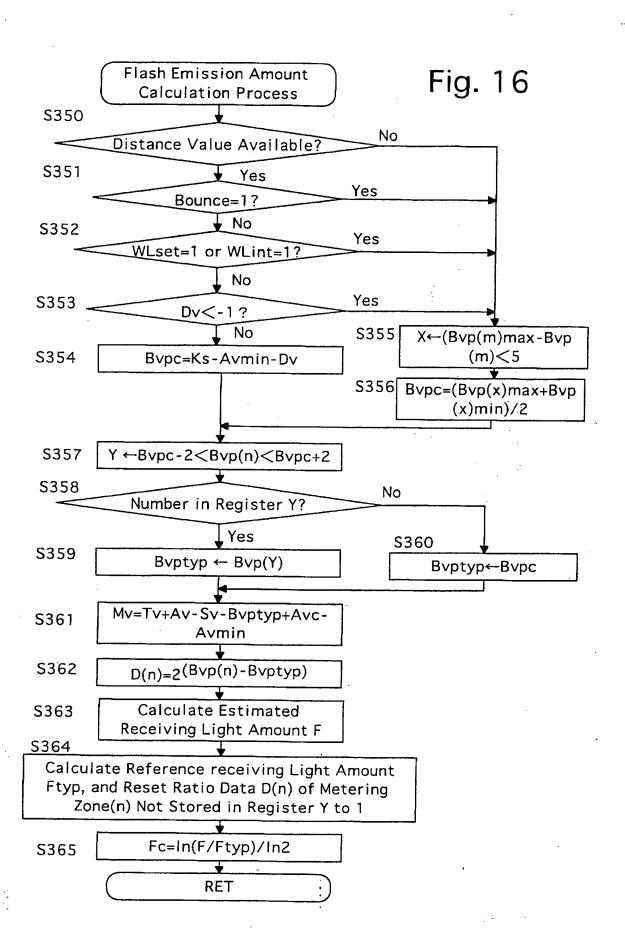












RET

Exposure Process

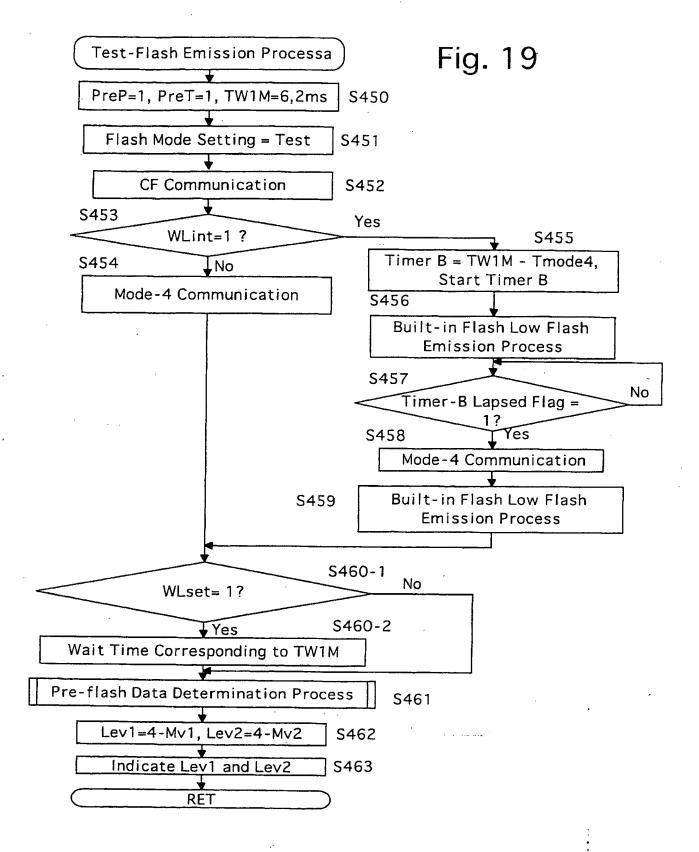
Pm2=0, Pm3=1, Pm2&Pm3 Output Mode

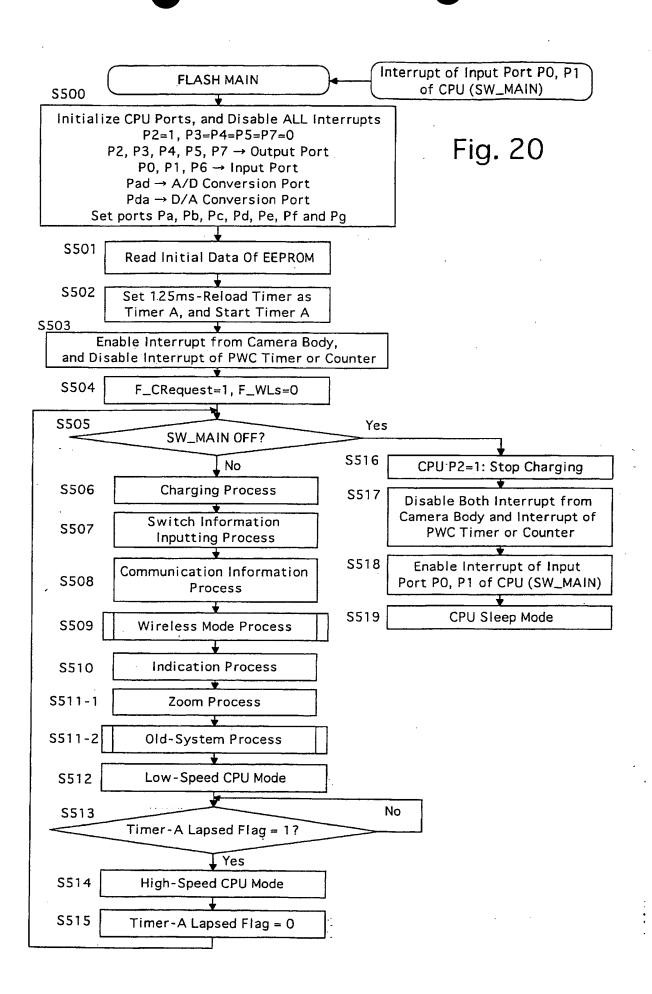
Fig. 17

S400

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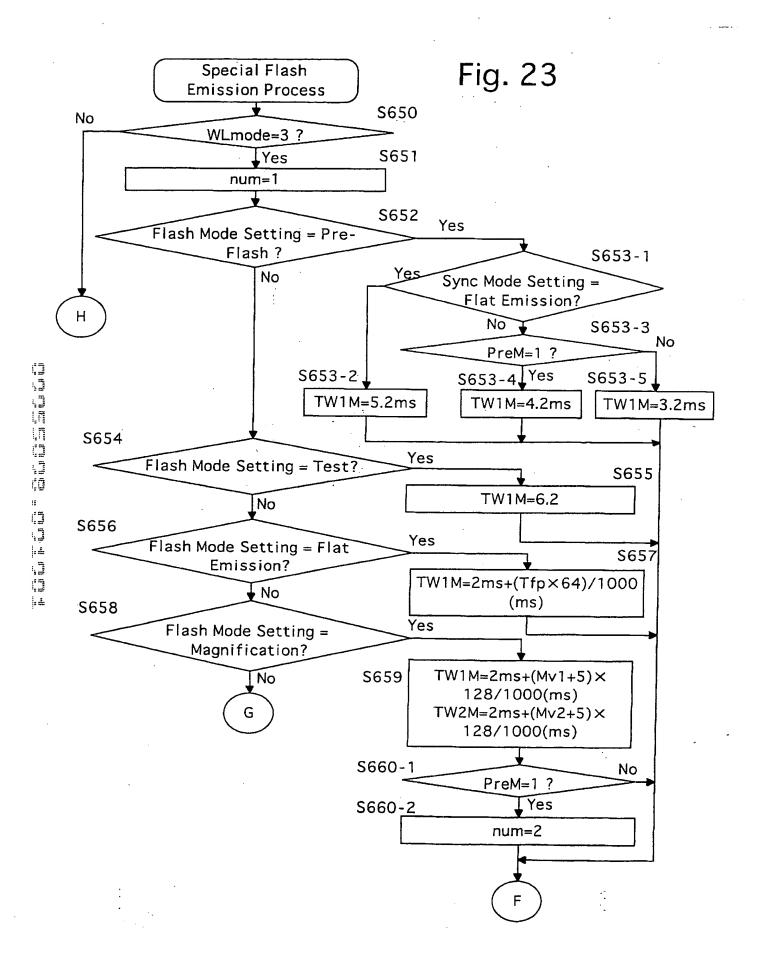
.]

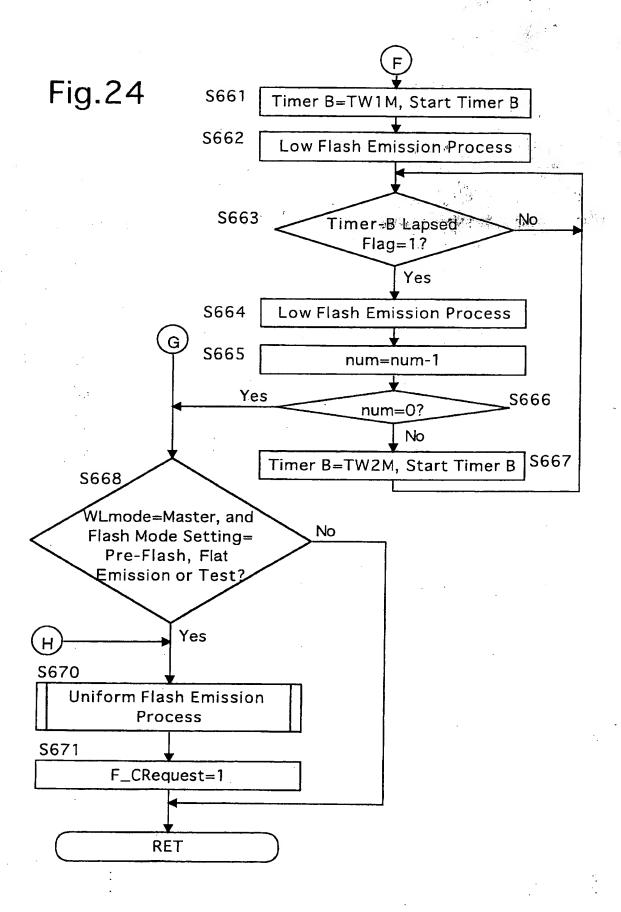


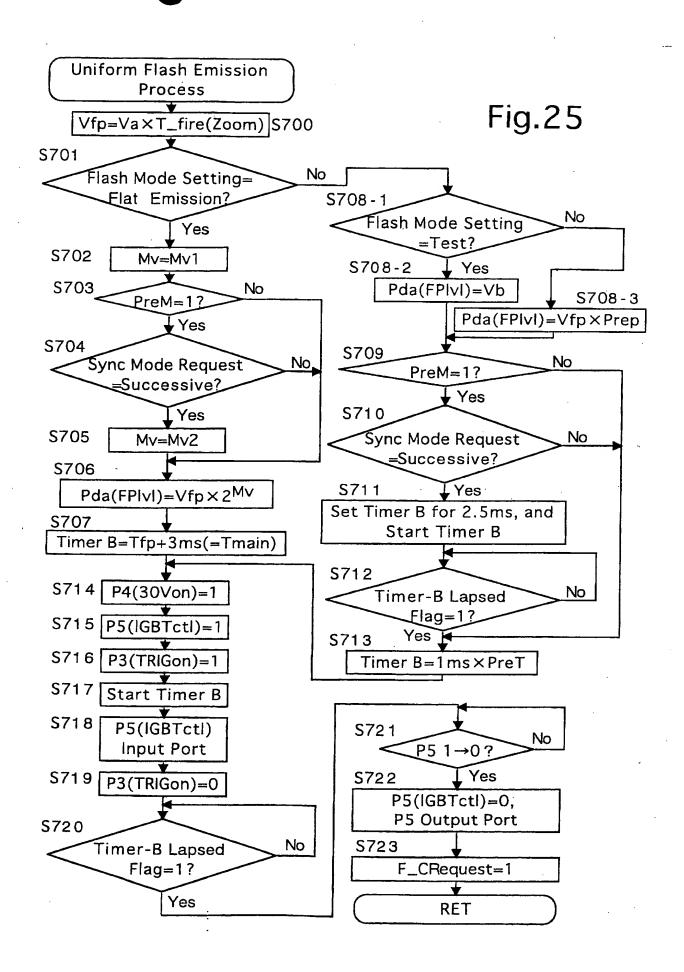


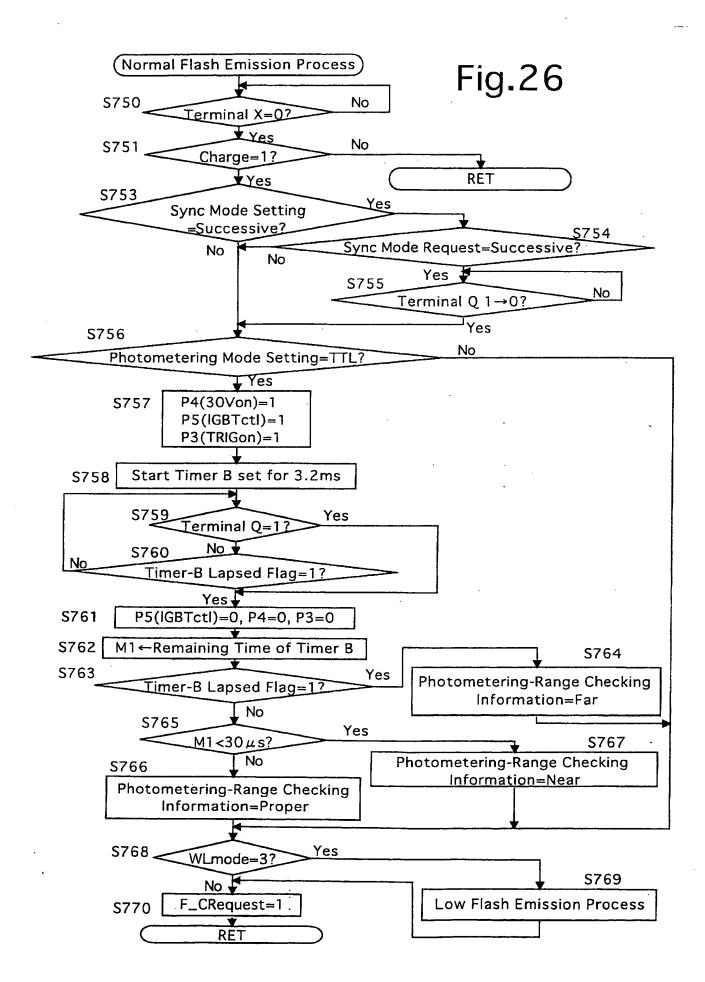
1.3

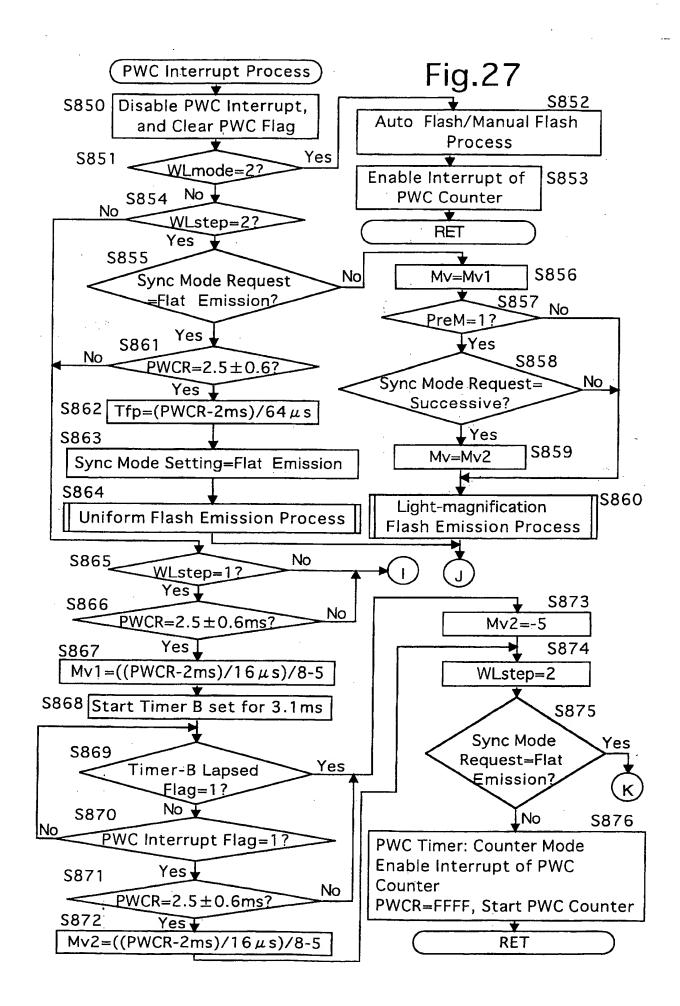
13

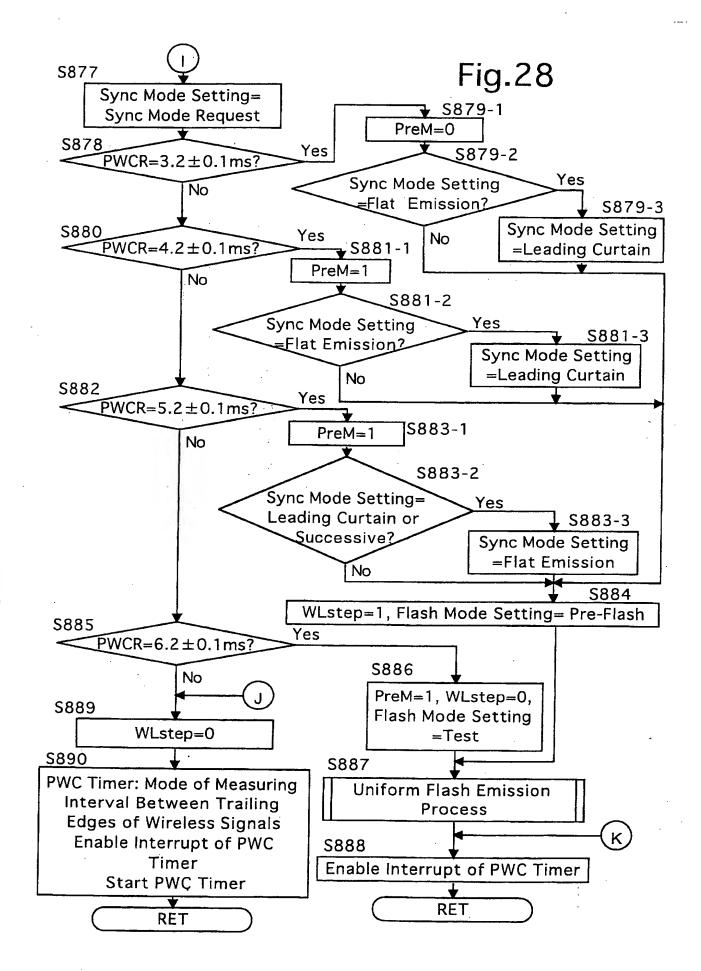












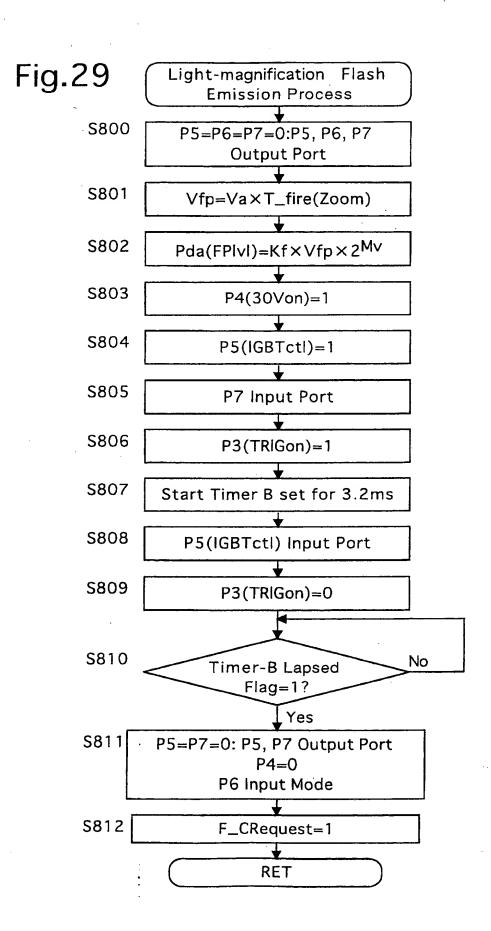


Fig.30

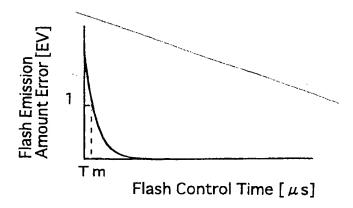
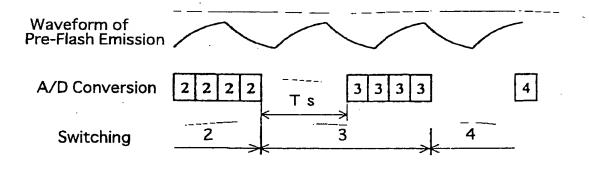


Fig.31



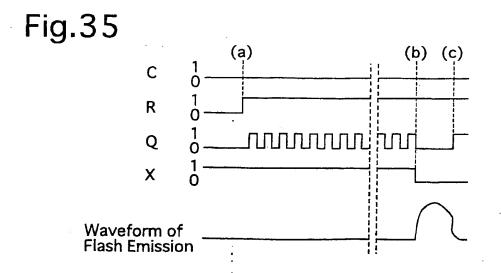


Fig.32

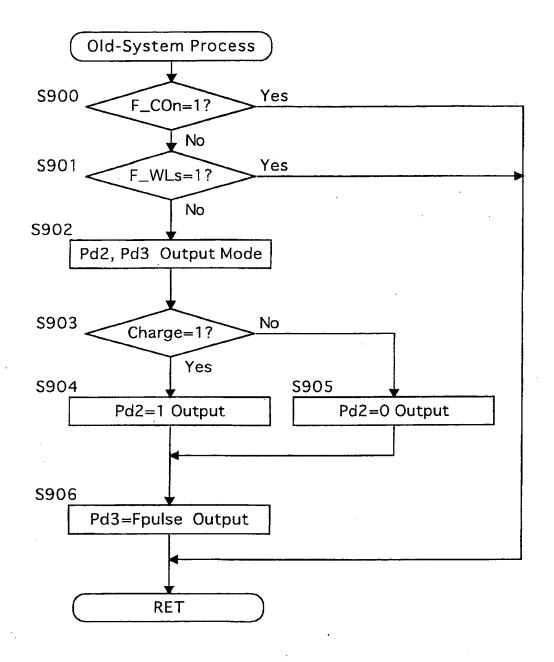


Fig.33

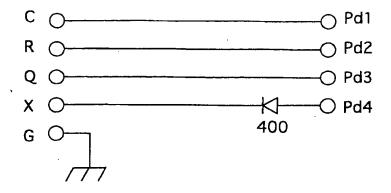


Fig.34

